The Automatic Generation of Loop Invariants using SMT Solvers

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Abstract

Loop invariant is a key and fundamental concept in the field of programming languages and formal verification. While the majority of programmers are not familiar with it. In this survey, we will introduce the concept of loop invariant and how to generate loop invariants automatically using SMT solvers.

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1 Introduction

A loop invariant is a condition or property that holds true before and after each iteration of a loop.

Loop invariants are of fundamental importance to the field of programming languages. They are used in the design of programming languages, compilers, and static analysis tools. They are also used in the verification of programs, which is a major area of research in programming languages.

Understanding loop invariants is also beneficial to programmers. Just as Dijkstra had suggested (he called it "constructive approach", see [1]), it helps them write correct programs and reason about the behavior of their code.

However, the concept and idea of loop invariants are not widespread beyond the field of programming languages and formal verification. This survey aims to give a brief introduction to loop invariants and generate loop invariants automatically using SMT solvers.

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2 Prerequisites and Motivation

2.1 Loop Invariant

Formal definition of loop invariant is as follows:

$$\frac{\{C \land I\} \text{ body } \{I\}}{\{I\} \text{ while } (C) \text{ body } \{\neg C \land I\}}$$

It is presented in Hoare Logic, and its meaning is that if the condition C and the invariant I hold before the loop, and if the body of the loop preserves the invariant, then the invariant will hold after the loop. The invariant I is called a loop invariant if it holds before and after each iteration of the loop.

Loop invariant is a major tool to reason about and formalize the behaviour of loops. Just as in semi-automatic program verification tools such as Dafny [4], only by annotating the loop invariant can Dafny prove the correctness of the program.

2.2 SMT Solver

SMT solvers are widely used in the field of programming languages and formal verification. They are used to solve the satisfiability of logical formulas.

We can regard SMT solvers as declarative programming languages. We can write logical formulas in SMT solvers, and then the SMT solvers will tell us whether the logical formulas are satisfiable or not. We just need to focus on what we want to do, and we don't need to care about how to solve the logical formulas.

2.3 Motivation of Automatic Generation of Loop Invariants

When we debug a program, we tend to take a big-to-small approach, i.e., we generally consider whether some large part of this program is working as expected. However, we find that many times the problem arises in the handling of a very small detail. As a result, it is also of great significance to garantee the correctness of some basic properties of a program (for example, an array index is always in the range of the array). They are simple, but they exist in almost every part of the program. It is definitely a waste of time to verify them manually(that is, manually type in the pre-/postcondition and the loop invariant for every loop) because of the simplicity and the large quantity.

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So, it naturally comes to our mind that it would be nice if we can use machines to automatically generate those loop invariants.

3 Automatic Generation of Loop Invariants using SMT Solvers

3.1 Introduction

Based on SAT solver, SMT solver expand the ability of SAT solver to handle first-order logic. SMT solver can handle the satisfiability of logical formulas with quantifiers, which is beyond the ability of SAT solver. It can also handle the satisfiability of logical formulas with some specific theories, such as the theory of linear integer arithmetic, the theory of real arithmetic, the theory of arrays, etc.

3.2 An Example

Let's consider the following program:

```
int i = 0;
while(i < 10) {
    i = i + 1;
}
assert(i == 10);
```

An obvious loop invariant is $0 \le i \le 10$.

We want to automatically generate the loop invariant, so now we denote it as Inv(i). then we can get the following logical formula:

 $\{Inv(i) \land i < 10\}\ i = i + 1\ \{Inv(i)\}\$

 $\overline{\{Inv(i)\}}$ while (i < 10) i = i + 1 $\{\neg(i < 10) \land Inv(i)\}$ This cannot be understanded by SMT solvers, because it

contains the while loop, which is not a boolean expression. We need to encode the while loop into boolean expres-

sions: $\forall i.i = 0 \Rightarrow Inv(i)$ $\forall i.Inv(i) \land i < 10 \Rightarrow Inv(i+1)$ $\forall i.Inv(i) \land not(i < 10) \Rightarrow i = 10$

Then we type in these formula to z3, a famous SMT solver, then get the following result:

sat

The simplified result is $x \le 10$. Acually we can find that it is a weaker version of the loop invariant we have found.

3.3 The solving strategy of SMT solver

Although we can just regard SMT solver as a black box, it is still necessary to know some basic knowledge about how SMT solver works.

So far, most SMT solvers are using lazy approaches. We can regard them as the integration of SAT solvers and one or more theory solvers. They will first use SAT solvers to check the satisfiability of the logical formulas without considering the theory. If the logical formulas are satisfiable, then they will use the theory solvers to check whether the logical formulas are satisfiable.

Although SAT problems are NP-complete, the SAT solvers are pretty efficient. SAT solvers are using some heuristic methods to accelerate the process, such as the conflict-driven clause learning (CDCL) algorithm.

In the above example, the key uninterpreted function *Inv* is solved by the uninterpreted function theory solver. The essence of the solution method is to construct a series of equivalence classes by means of a concurrent lookup set algorithm, and from these equivalence classes the enumerated form of the function is constructed.

For this example, the uninterpreted function Inv(x) is constructed as two enumerated equivalence classes, $[-\infty, 10]$ and $[11, +\infty]$.

3.4 Drawback and Improvement

It is often difficult for the SMT solver to tackle uninterpreted functions.

Luckily, there are several ways to accelerate the process. Affine inequality (a template of loop invariant with undetermined coefficients), Guess and Check (heuristically guessing loop invariants and check them), Farkas' Lemma (eliminate the universal quantifier), etc.

Typically, Guess and check is a simple but pretty effective method.

For example, in the previous example, we can find some special value 0, 1, 10and special relation $i = 0, i \le 10, i = 0$. Then we can generate some loop invariant candidates according to some variation or combination of the special thing above, and check them using SMT solvers.

Moreover, we can use some heuristic methods, such as the one proposed by Gulwani et al. [3] or Carlo A. Furia and Bertrand Meyer [2]. Most of their ideas are straightforward, and we are able to get those ideas naturally as we practice the invariant construction ourselves

4 Conclusion

In this survey, we have explaind basic information about loop invariant, SMT solver. We have also introduced how to use SMT solver to automatically generate loop invariants. After that some methods are mentioned to accelerate the process. The Automatic Generation of Loop Invariants using SMT Solvers

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